



81837DMW  
Customer No. 01333

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Lucas P. Curtis, et al

CCD CLOCK ALIGNMENT CIRCUIT  
USING A FREQUENCY LOCKED  
CLOCK MULTIPLIER

Serial No. US 09/870,336

Filed 30 May 2001

Commissioner for Patents  
Washington, D.C. 20231

Sir:

Group Art Unit:

Batch:

Allowed:

Examiner:

I hereby certify that this correspondence is being deposited today with the United States Postal Service as first class mail in an envelope addressed to Commissioner for Patents, Washington, D.C. 20231.

Robin G. Reeves  
Robin G. Reeves

7/13/01  
Date

LETTER TO THE OFFICIAL DRAFTSPERSON

Enclosed are 4 sheets of formal drawings depicting Figure(s) 1-6.  
Please substitute these drawings for those currently on file in the subject application.

The Commissioner is hereby authorized to charge any fees in connection with this communication to Eastman Kodak Company Deposit Account No. 05-0225. **A duplicate copy of this letter is enclosed.**

Respectfully submitted,

David M. Woods  
David M. Woods  
Attorney for Applicants  
Registration No. 27,171

DMW/RGR  
Telephone: (716) 477-5256  
Facsimile: (716) 477-4646